PATENT

Practitioner's Docket No.: 791_200 CON

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Yukihisa TAKEUCHI, Hiroyuki TSUJI, Kazumasa KITAMURA

and Yoshinori YAMAGUCHI

Filed: Concurrently herewith

For: MULTILAYER BOARD HAVING PRECISE PERFORATIONS AND

CIRCUIT SUBSTRATE HAVING PRECISE THROUGH-HOLES

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 23, 2004 under "EXPRESS MAIL" mailing label number EV 406992163 US

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INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. These references were cited during prosecution of parent application U.S. Ser. No. 10/160,998, and copies of each of the references can be found in the PTO file for the parent application.

The above information is presented so that the Patent and Trademark Office may, in the first instance, determine any materiality thereof to the claimed invention. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that these listed references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Respectfully submitted,

February 23, 2004

Date

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT					First Named Inventor Art Unit				Yukihisa TAKEUCHI							
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Examiner: Date Considered:

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